

Application No. 09/883,539

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IN THE SPECIFICATION

Please replace Paragraph No. [28] with the following:

[28] Continuing, the output of the offset QPSK signal modulator 81 is coupled to the frequency modulator 82. Referring to Fig. 3, the output of the FIR filter/scalar 41 is coupled to one input of an adder 51. The other input to the adder 51 receives a tuning signal, K, generated by a controller 53. As explained in more detail below, the tuning signal K functions to fine tune the output generated by the PLL 33 to the desired carrier frequency. In operation, the fine tune signal K is predetermined and set to the desired value. In addition, the output signal of the OQPSK signal modulator 81, which represents the modulation data, causes the ~~sigma-delta modulator 32~~ frequency modulator 82 to continually adjust the output frequency of the PLL 33 such that the output signal generated by the PLL 33 is frequency modulated in accordance with the output signal of the OQPSK modulator 81.